

Docket No.: 057454-0060

PATENT



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Isao MINEMATSU

RESPONSE UNDER 37 CFR 1.116
EXPEDITED PROCEDURE

Application No.: 09/819,990

Customer No.: 20277
Confirmation No.: 3710

Filed: March 29, 2001

Group Art Unit: 2183

Examiner: Daniel H. Pan

Title: MICROPROCESSOR EXECUTING DATA TRANSFER BETWEEN MEMORY
AND REGISTER AND DATA TRANSFER BETWEEN REGISTERS IN RESPONSE
TO SINGLE PUSH/POP INSTRUCTION

AMENDMENT UNDER 37 CFR 1.116

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the Office Action mailed December 21, 2004, please amend the
above-identified application as follows: